ElGamal Signature Card

Breaking Smart Card Based ElGamal Signature and Its Variants Asiacrypt96 Rump Session, 5 Nov. 1996

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Exploiting hardware faults

- Secret key algorithms
 - DES, IDEA, FEAL, etc --- broken by Biham & Shamir (18 Oct. & 30 Oct.)
 - □ (see also J-J Quisquater, 23 Oct.)
- Public key signature
 - RSA --- broken by
 - Bellcore (25 Sept., no details were published)
 - Nat. Uni. of Singapore (23 Oct.)

ElGamal family -- ???

Completing the big picture

 We show how to break smart card based

- ElGamal signature and
- all its variants,

by (temporally) falsifying part of the circuitry in the smart card

• Time: 31 Oct. 1996

The Attack Model

 An attacker, who is in possession of a target smart card,

may introduce faults into tamperproof hardware in the smart card,

say by exposing it to certain physical effects (heat, laser, pressure, radiation, etc)



The attack may then compromise a secret in the smart card.



- a RSA signature-generation / decryption key
- certain authentication / identification cards

ElGamal Signature

Alice's keys $\square X_a$ --- secret key for signature generation $y_a = g^{x_a} \mod p$ --- public key Alice's signature on a message m is a pair of number (r,s): $x \in_{R} [1, ..., p-1]$ $r = g^x \mod p$ $\Box s = \frac{hash(m) - x_a \cdot r}{mod(p-1)}$

Idea for finding x_a

• An attacker may figure out x_a by making the "random" number x predictable, through the falsification of part of the hardware software pseudo-random number generator --- suppressing the contents of the status register that stores the current "seed" number hard pseudo-random number generator --- suppressing its output

Two possibilities

- software pseudo-random number generator
 - suppressing the contents of the status register that stores the current "seed" number
- hard pseudo-random number generator
 - suppressing its output



Main idea

- suppress the contents of the status register that stores the current seed for the PRG
- so that only a fixed number, such as the all-1 value, is (temporarily) available to the CPU
- collect the output (r,s) of the smart card





$$x_a = \frac{hash(m) - s \cdot x}{r} \operatorname{mod}(p-1)$$







$$x_a = \frac{hash(m) - s \cdot x}{r} \operatorname{mod}(p-1)$$

Another attack

- Some smart cards have a built-in PRG that would produce a constant output when a lower than normal voltage is supplied.
- For such a smart card, an attacker may obtain Alice's secret key x_a by asking the card to sign 2 different messages while keeping the voltage low.

Another attack (cnt'd)

• We will have $s_{1} = \frac{hash(m_{1}) - x_{a} \cdot r}{x} \mod(p-1)$ $s_{2} = \frac{hash(m_{2}) - x_{a} \cdot r}{x} \mod(p-1)$

• Hence $x = \frac{hash(m_1) - hash(m_2)}{s_1 - s_2} \mod p - 1$

$$x_a = \frac{hash(m_1) - x \cdot s_1}{r} \mod p - 1$$



Attacking RSA by NUS

Nat Uni Singapore method
Finding an RSA decryption / signature generation key by

complementing *one or a few* bits at random positions in the unknown decryption/signature-generation key,

which is harder than suppressing the entire contents of a register !

A comparison with RSA (cnt'd)

 Therefore, compared to the attacker on RSA signature / decryption card, our attack seems

- simpler
- more feasible to mount
 - say, on an entire data-bus
- but, harder to prevent
 - reason --- (r,s) is a perfectly valid signature !

